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تهران پاساژ امجد طبقه1 واحد 16

02166766957 -02166766927

info@atrinelec.com

@atrinelec

LM833

Dual Audio Operational Amplifier

General Description

The LM833 is a dual general purpose operational amplifier designed with particular emphasis on performance in audio systems.

This dual amplifier IC utilizes new circuit and processing techniques to deliver low noise, high speed and wide bandwidth without increasing external components or decreasing stability. The LM833 is internally compensated for all closed loop gains and is therefore optimized for all preamp and high level stages in PCM and HiFi systems.

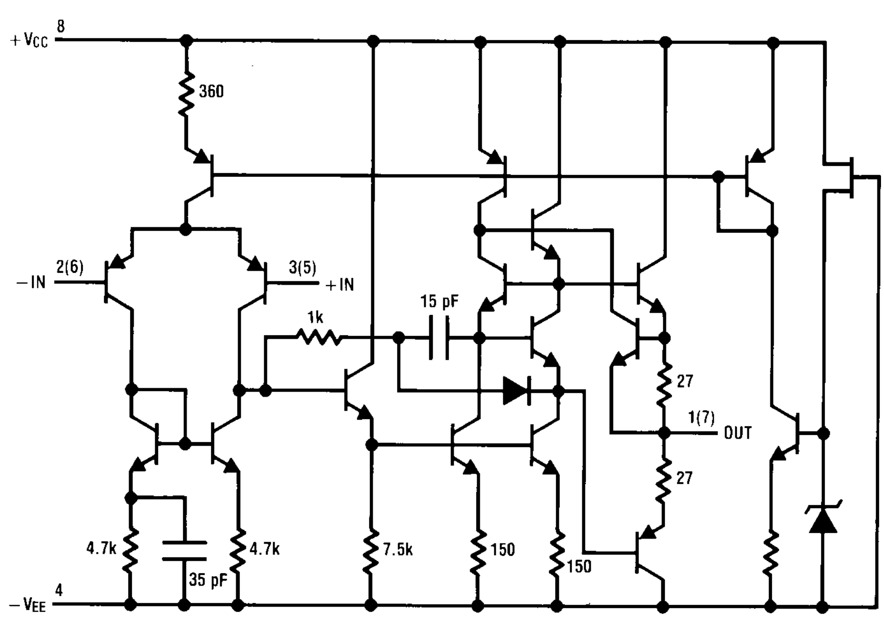
The LM833 is pin-for-pin compatible with industry standard dual operational amplifiers.

Features

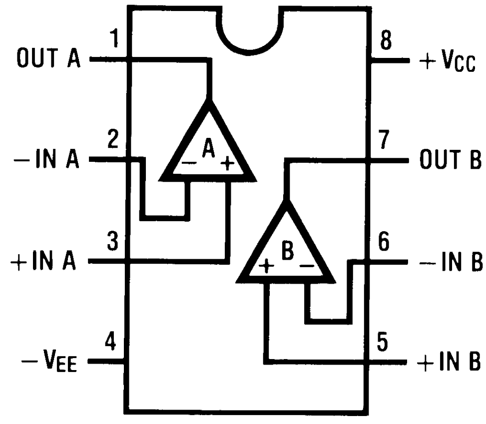
- Wide dynamic range: >140dB
- Low input noise voltage: $4.5nV/\sqrt{Hz}$
- High slew rate: 7 V/μs (typ); 5V/μs (min)
- High gain bandwidth: 15MHz (typ); 10MHz (min)
- Wide power bandwidth: 120KHz
- Low distortion: 0.002%
- Low offset voltage: 0.3mV
- Large phase margin: 60°
- Available in 8 pin MSOP package

Schematic Diagram

(1/2 LM833)



Connection Diagram



Order Number LM833M, LM833MX, LM833AM, LM833AMX, LM833N, LM833MM or LM833MMX
See NS Package Number
M08A, N08E or MUA08A

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

Supply Voltage V_{CC}–V_{EE}	36V
Differential Input Voltage (Note 3) V_I	±30V
Input Voltage Range (Note 3) V_{IC}	±15V
Power Dissipation (Note 4) P_D	500 mW
Operating Temperature Range T_{OPR}	−40 ~ 85°C
Storage Temperature Range T_{STG}	−60 ~ 150°C
Soldering Information	
Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package (SOIC and MSOP)	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C
ESD tolerance (Note 5)	1600V

DC Electrical Characteristics (Note 1, Note 2)

(T_A = 25°C, V_S = ±15V)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{OS}	Input Offset Voltage	R_S = 10Ω		0.3	5	mV
I_{OS}	Input Offset Current			10	200	nA
I_B	Input Bias Current			500	1000	nA
A_V	Voltage Gain	R_L = 2 kΩ, V_O = ±10V	90	110		dB
V_{OM}	Output Voltage Swing	R_L = 10 kΩ	±12	±13.5		V
		R_L = 2 kΩ	±12	±13.4		V
V_{CM}	Input Common-Mode Range		±12	±14.0		V
CMRR	Common-Mode Rejection Ratio	V_{IN} = ±12V	80	100		dB
PSRR	Power Supply Rejection Ratio	V_S = 15 ~ 5V, −15 ~ −5V	80	100		dB
I_Q	Supply Current	V_O = 0V, Both Amps		5	8	mA

AC Electrical Characteristics

(T_A = 25°C, V_S = ±15V, R_L = 2 kΩ)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
SR	Slew Rate	R_L = 2 kΩ	5	7		V/μs
GBW	Gain Bandwidth Product	f = 100 kHz	10	15		MHz
V_{NI}	Equivalent Input Noise Voltage (LM833AM, LM833AMX)	RIAA, R_S = 2.2 kΩ (Note 6)			1.4	μV

Design Electrical Characteristics

(T_A = 25°C, V_S = ±15V)

The following parameters are not tested or guaranteed.

Symbol	Parameter	Conditions	Typ	Units
$\Delta V_{OS}/\Delta T$	Average Temperature Coefficient of Input Offset Voltage		2	μV/°C
THD	Distortion	R_L = 2 kΩ, f = 20~20 kHz V_{OUT} = 3 Vrms, A_V = 1	0.002	%
e_n	Input Referred Noise Voltage	R_S = 100Ω, f = 1 kHz	4.5	$nV/\sqrt{Hz}$

Symbol	Parameter	Conditions	Typ	Units
i_n	Input Referred Noise Current	f = 1 kHz	0.7	$pA/\sqrt{Hz}$
PBW	Power Bandwidth	V_O = 27 V_{pp}, R_L = 2 kΩ, THD ≤ 1%	120	kHz
f_U	Unity Gain Frequency	Open Loop	9	MHz
φ_M	Phase Margin	Open Loop	60	deg
	Input Referred Cross Talk	f = 20~20 kHz	–120	dB

Note 1: *Absolute Maximum Ratings* indicate limits beyond which damage to the device may occur. *Operating Ratings* indicate conditions for which the device is functional, but do not guarantee specific performance limits. *Electrical Characteristics* state DC and AC electrical specifications under particular test conditions which guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given, however, the typical value is a good indication of device performance.

Note 2: All voltages are measured with respect to the ground pin, unless otherwise specified.

Note 3: If supply voltage is less than ±15V, it is equal to supply voltage.

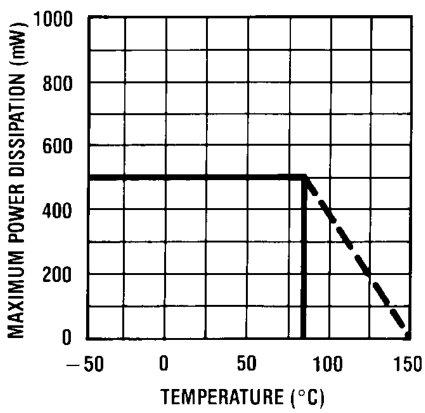
Note 4: This is the permissible value at $T_A \leq 85°C$.

Note 5: Human body model, 1.5 kΩ in series with 100 pF.

Note 6: RIAA Noise Voltage Measurement Circuit

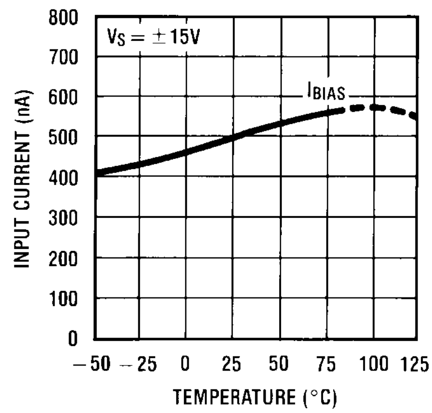
Typical Performance Characteristics

Maximum Power Dissipation vs Ambient Temperature



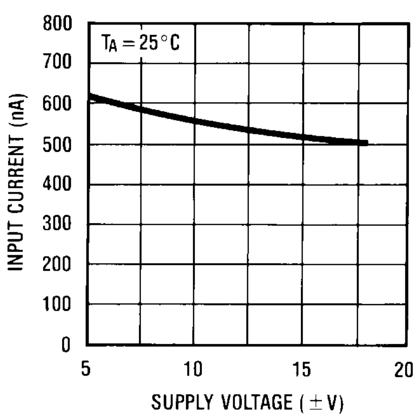
521804

Input Bias Current vs Ambient Temperature



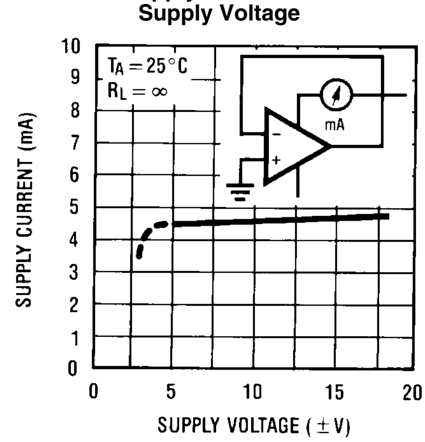
521805

Input Bias Current vs Supply Voltage



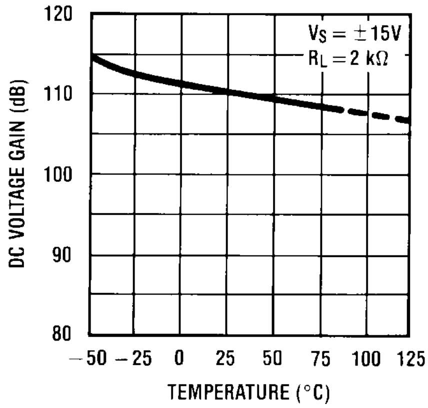
521806

Supply Current vs Supply Voltage



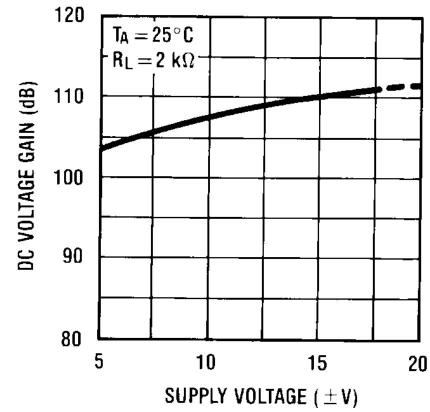
521807

DC Voltage Gain vs Ambient Temperature



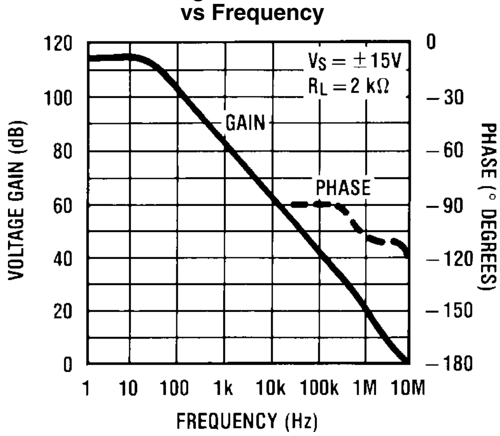
521808

DC Voltage Gain vs Supply Voltage



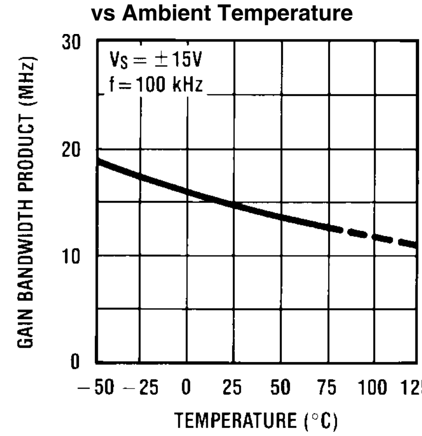
521809

Voltage Gain & Phase vs Frequency



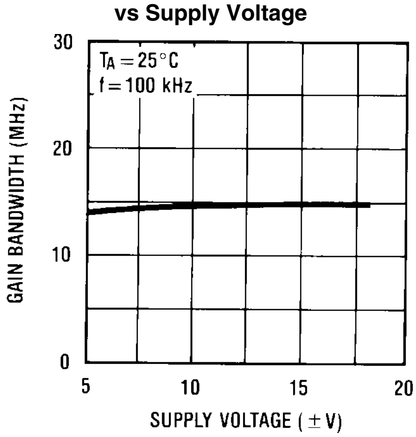
521810

Gain Bandwidth Product vs Ambient Temperature



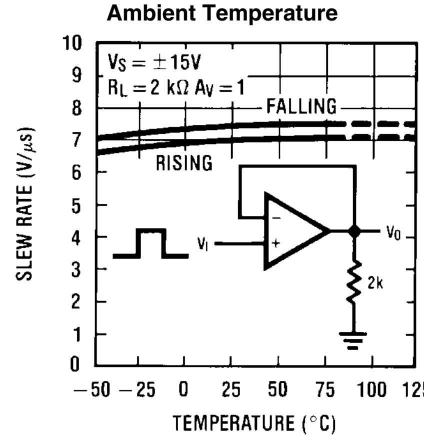
521811

Gain Bandwidth vs Supply Voltage



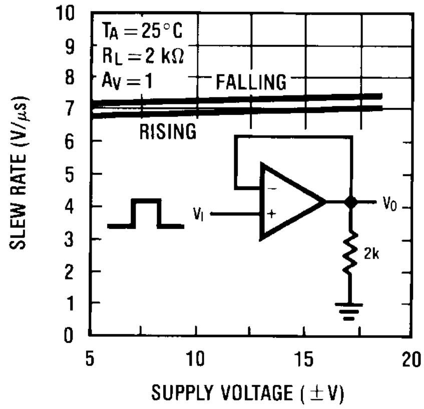
521812

Slew Rate vs Ambient Temperature



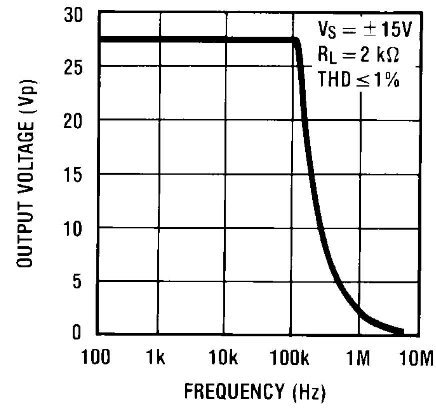
521813

Slew Rate vs Supply Voltage



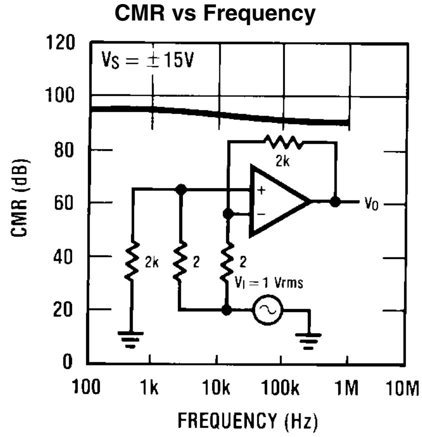
521814

Power Bandwidth



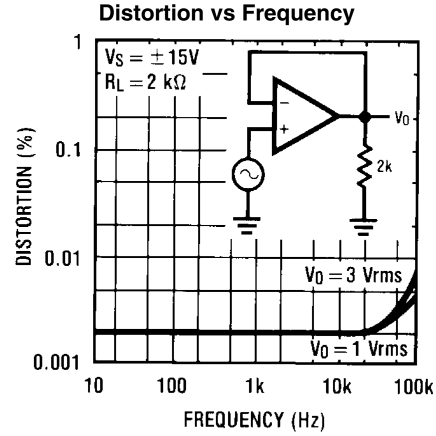
521815

CMR vs Frequency



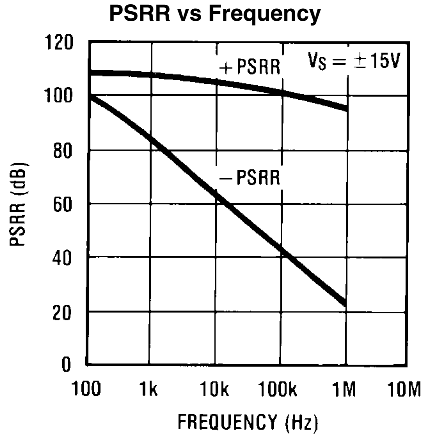
521819

Distortion vs Frequency



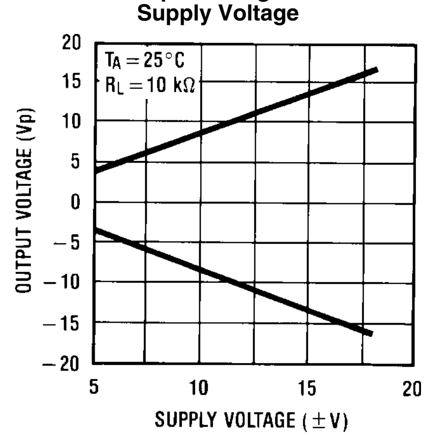
521820

PSRR vs Frequency



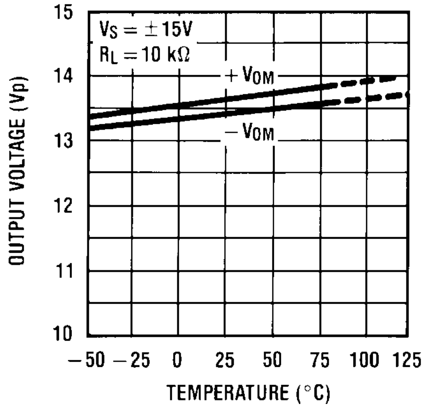
521818

Maximum Output Voltage vs Supply Voltage



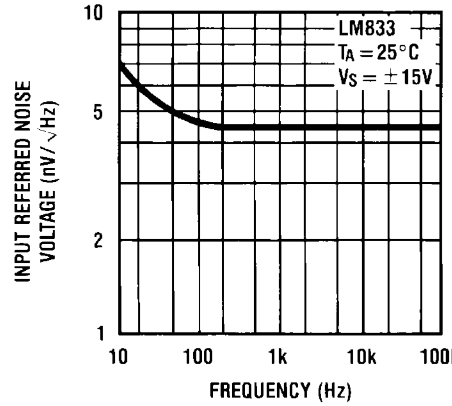
521816

Maximum Output Voltage vs Ambient Temperature



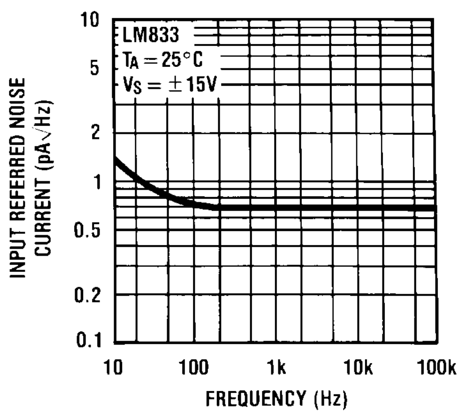
521817

Spot Noise Voltage vs Frequency



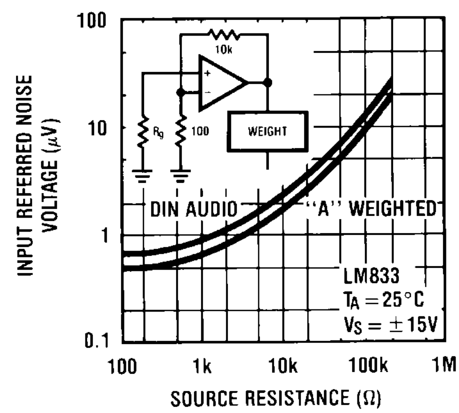
521821

Spot Noise Current vs Frequency



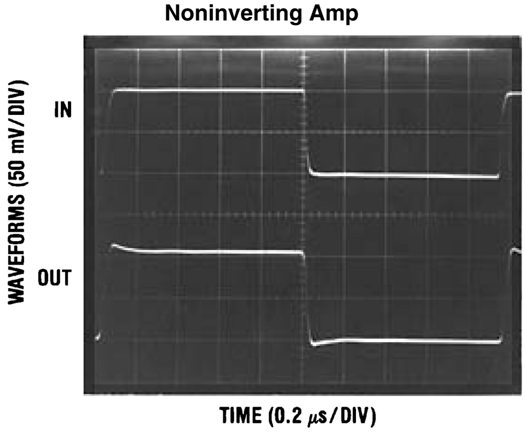
521822

Input Referred Noise Voltage vs Source Resistance



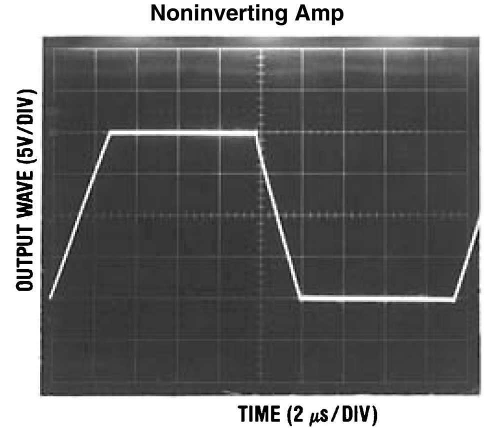
521823

Noninverting Amp



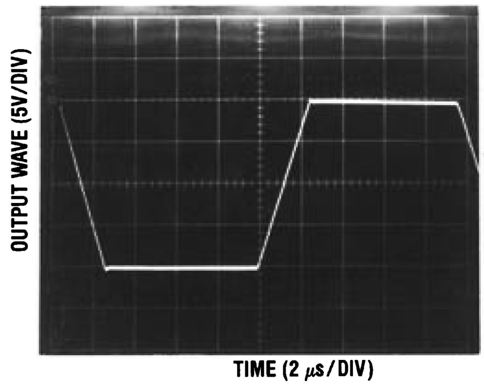
521824

Noninverting Amp



521825

Inverting Amp



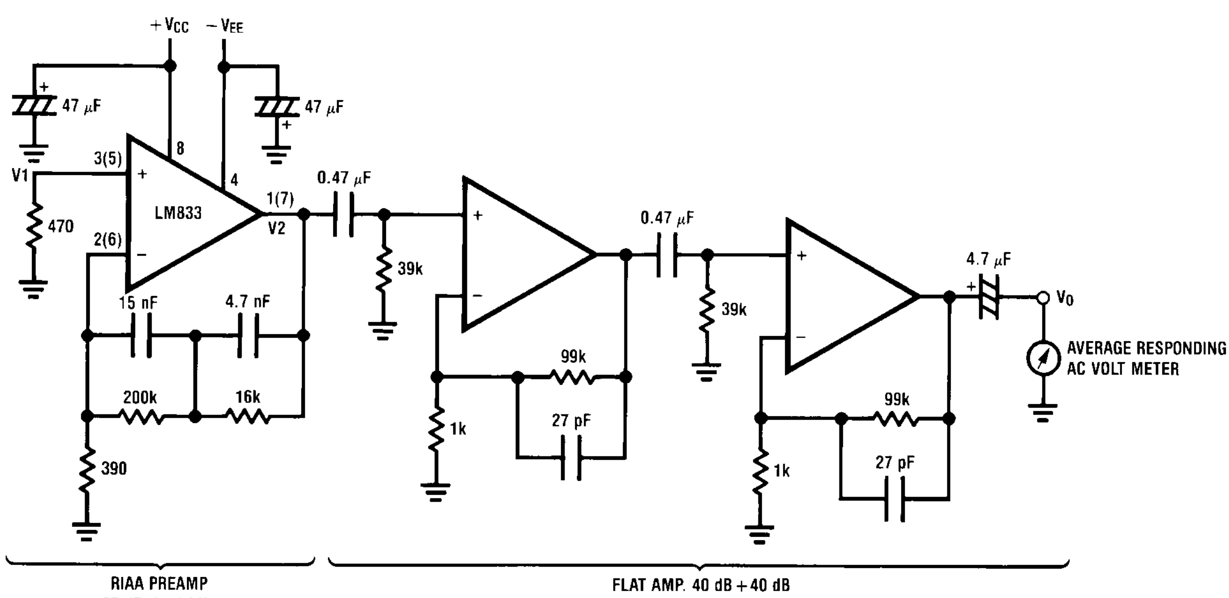
521826

Application Hints

The LM833 is a high speed op amp with excellent phase margin and stability. Capacitive loads up to 50 pF will cause little change in the phase characteristics of the amplifiers and are therefore allowable.

Capacitive loads greater than 50 pF must be isolated from the output. The most straightforward way to do this is to put a resistor in series with the output. This resistor will also prevent excess power dissipation if the output is accidentally shorted.

Noise Measurement Circuit



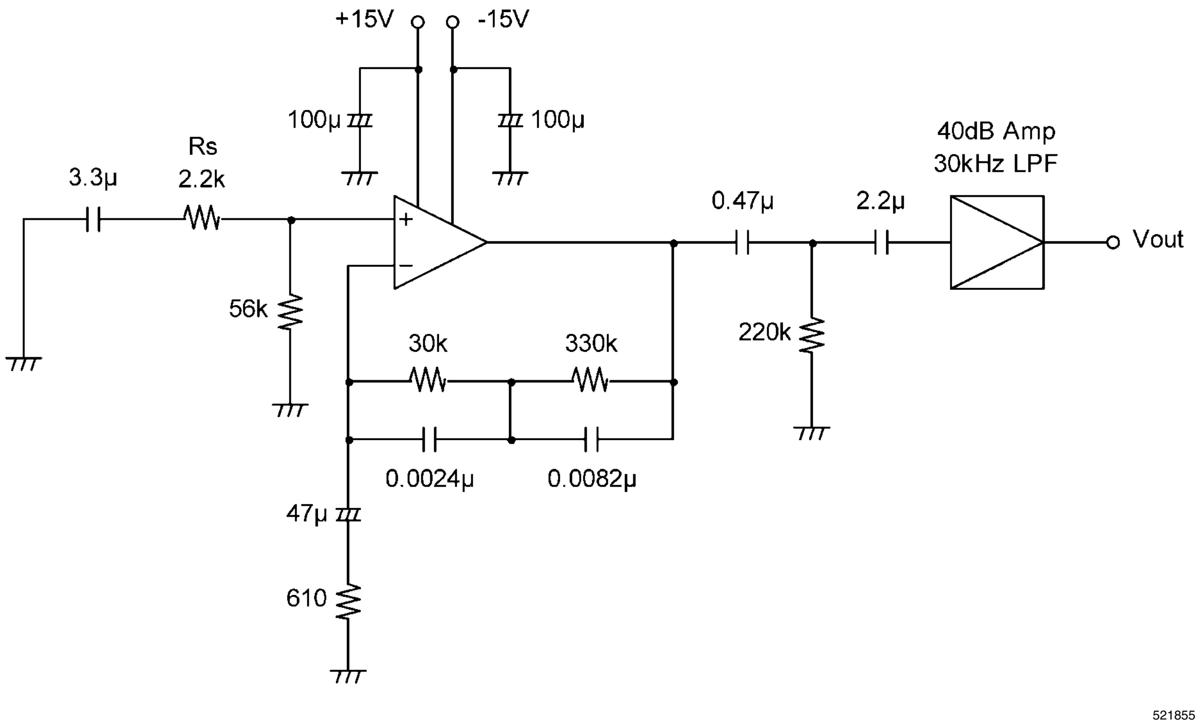
521827

Complete shielding is required to prevent induced pick up from external sources. Always check with oscilloscope for power line noise.

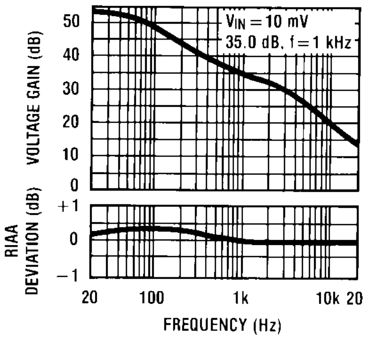
Total Gain: 115 dB @f = 1 kHz

Input Referred Noise Voltage: e_n = V0/560,000 (V)

RIAA Noise Voltage Measurement Circuit

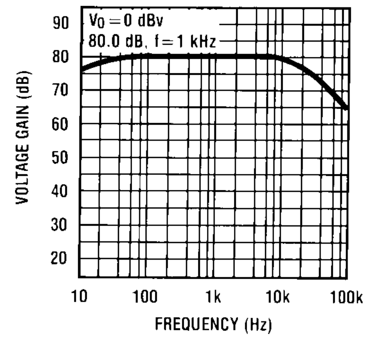


RIAA Preamp Voltage Gain, RIAA Deviation vs Frequency



521828

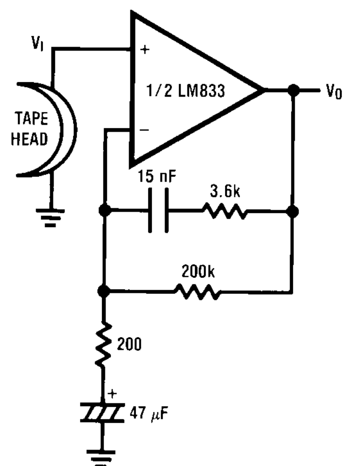
Flat Amp Voltage Gain vs Frequency



521829

Typical Applications

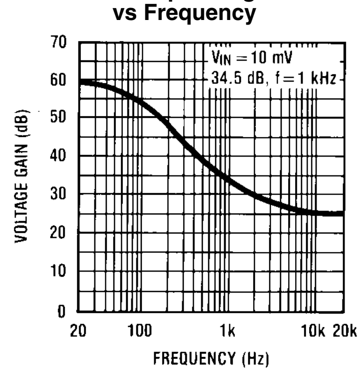
NAB Preamp



521830

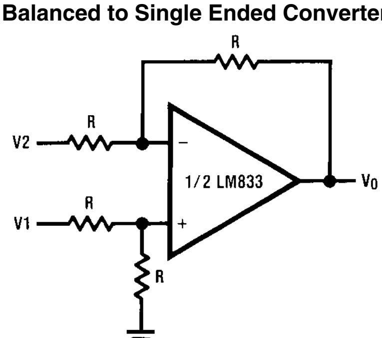
A_V = 34.5
F = 1 kHz
E_n = 0.38 μV
A Weighted

NAB Preamp Voltage Gain vs Frequency



521831

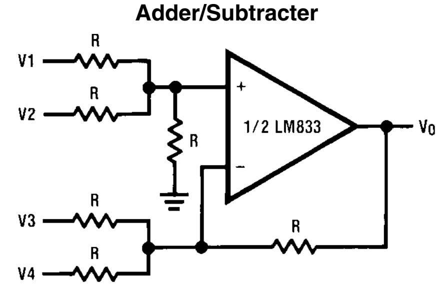
Balanced to Single Ended Converter



521832

V_O = V1–V2

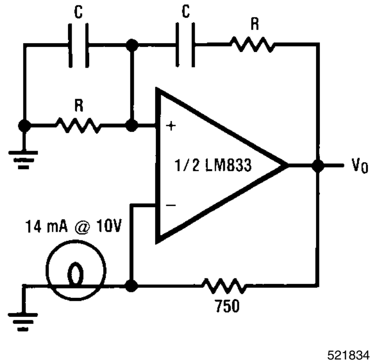
Adder/Subtracter



521833

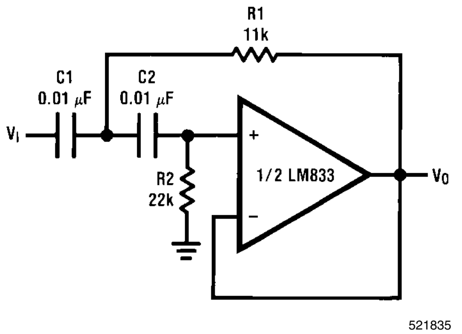
V_O = V1 + V2 – V3 – V4

Sine Wave Oscillator



$$f_o = \frac{1}{2\pi RC}$$

Second Order High Pass Filter (Butterworth)



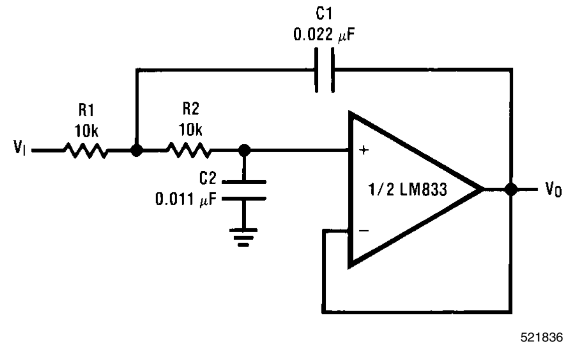
if C1 = C2 = C

$$R1 = \frac{\sqrt{2}}{2\omega_o C}$$

$$R2 = 2 \bullet R1$$

Illustration is f_0 = 1 kHz

Second Order Low Pass Filter (Butterworth)



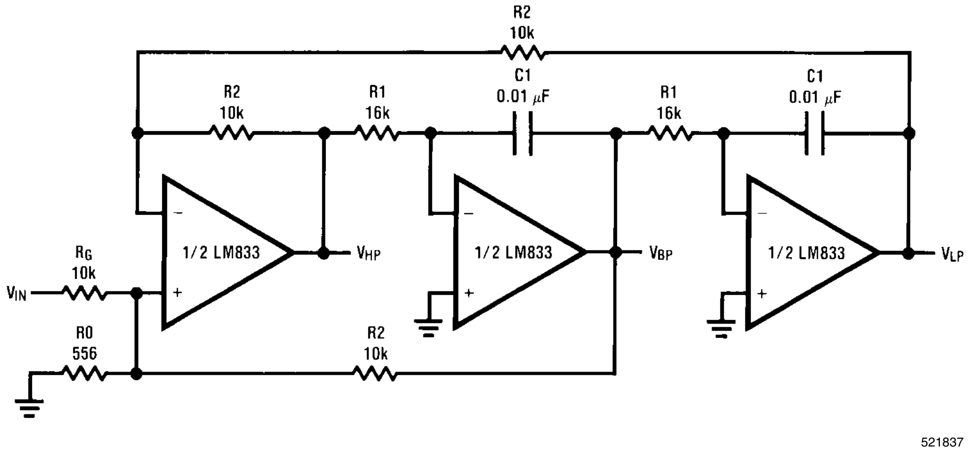
if R1 = R2 = R

$$C1 = \frac{\sqrt{2}}{\omega_o R}$$

$$C2 = \frac{C1}{2}$$

Illustration is f_0 = 1 kHz

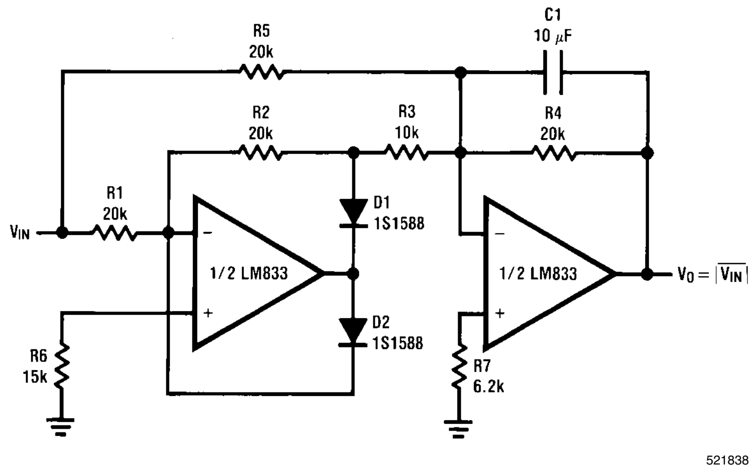
State Variable Filter



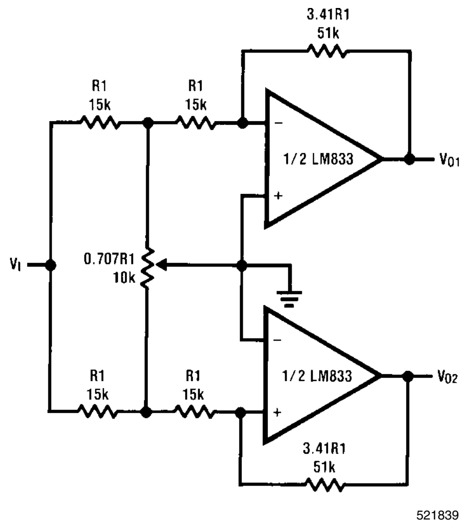
$$f_0 = \frac{1}{2\pi C1R1}, Q = \frac{1}{2}\left(1 + \frac{R2}{R0} + \frac{R2}{RG}\right), A_{BP} = QA_{LP} = QA_{LH} = \frac{R2}{RG}$$

Illustration is f_0 = 1 kHz, Q = 10, A_{BP} = 1

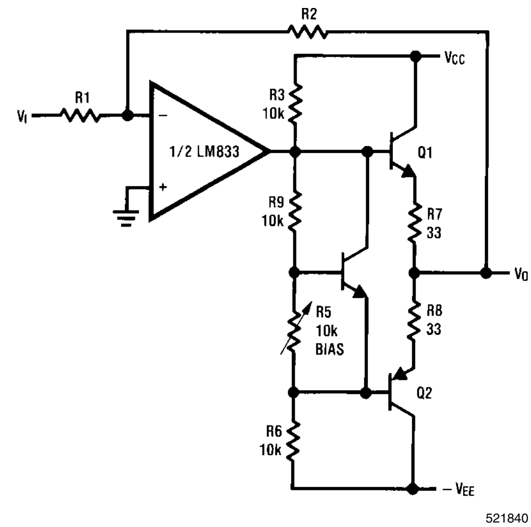
AC/DC Converter



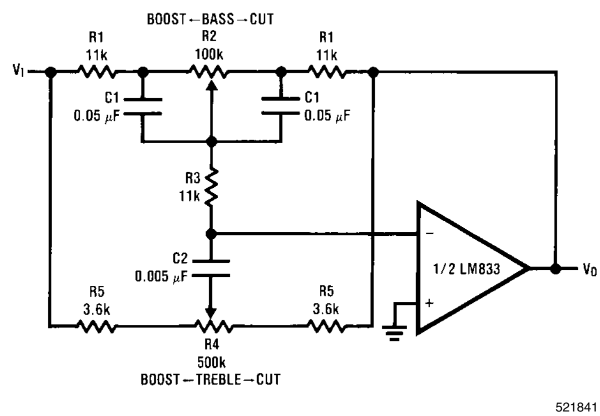
2 Channel Panning Circuit (Pan Pot)



Line Driver



Tone Control



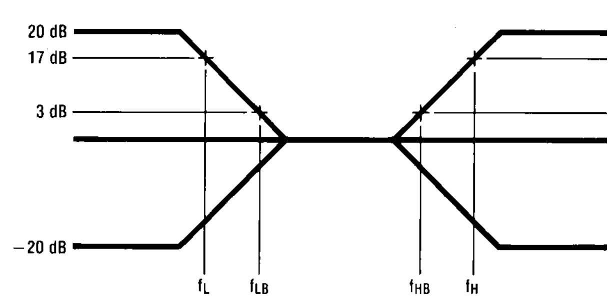
$$f_L = \frac{1}{2\pi R2C1},\ f_{LB} = \frac{1}{2\pi R1C1}$$

$$f_H = \frac{1}{2\pi R5C2},\ f_{HB} = \frac{1}{2\pi (R1 + R5 + 2R3)C2}$$

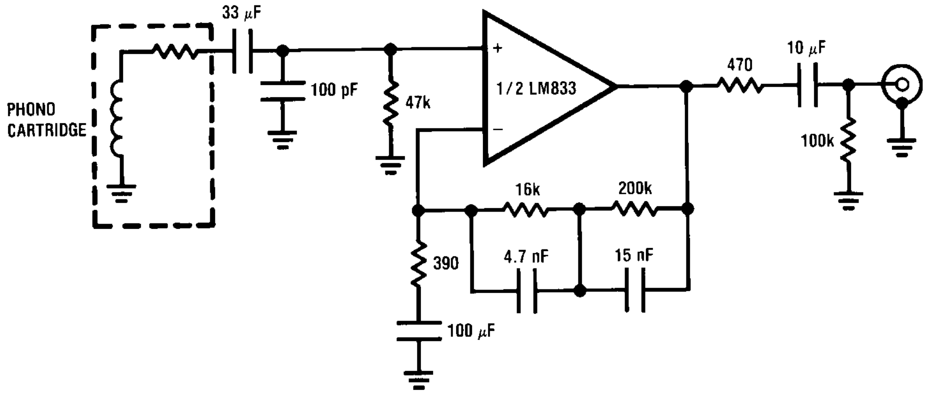
Illustration is:

f_L = 32 Hz, f_{LB} = 320 Hz

f_H =11 kHz, f_{HB} = 1.1 kHz



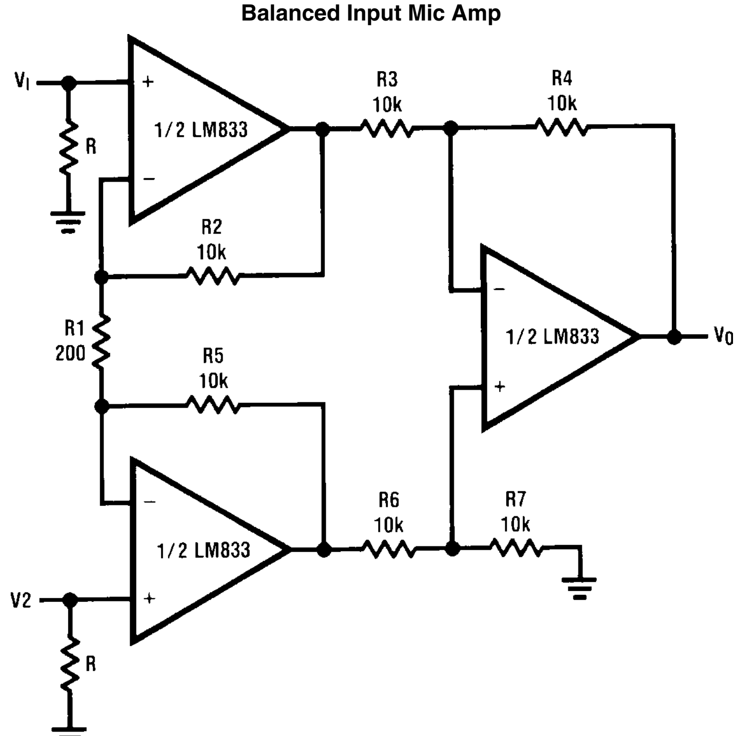
RIAA Preamp



521803

A_v = 35 dB
E_n = 0.33 μV
S/N = 90 dB
f = 1 kHz
A Weighted
A Weighted, V_{IN} = 10 mV
@f = 1 kHz

Balanced Input Mic Amp



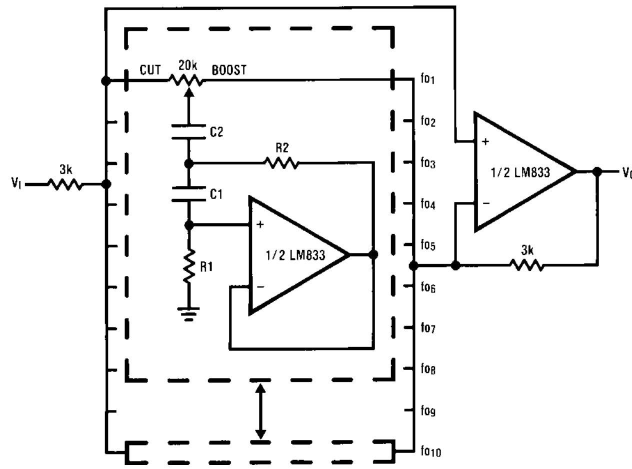
521843

If R2 = R5, R3 = R6, R4 = R7

$$V0 = \left(1 + \frac{2R2}{R1}\right)\frac{R4}{R3}(V2 - V1)$$

Illustration is:
V0 = 101(V2 – V1)

10 Band Graphic Equalizer

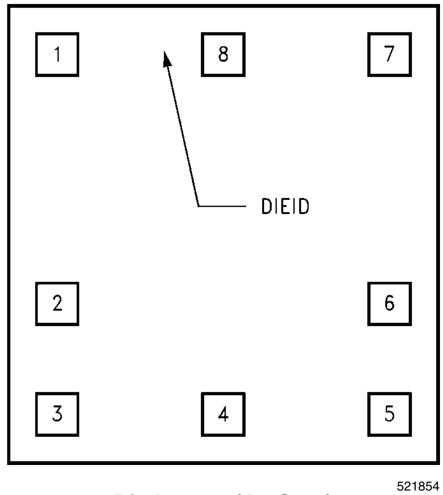


521844

fo (Hz)	C_1	C_2	R_1	R_2
32	0.12μF	4.7μF	75kΩ	500Ω
64	0.056μF	3.3μF	68kΩ	510Ω
125	0.033μF	1.5μF	62kΩ	510Ω
250	0.015μF	0.82μF	68kΩ	470Ω
500	8200pF	0.39μF	62kΩ	470Ω
1k	3900pF	0.22μF	68kΩ	470Ω
2k	2000pF	0.1μF	68kΩ	470Ω
4k	1100pF	0.056μF	62kΩ	470Ω
8k	510pF	0.022μF	68kΩ	510Ω
16k	330pF	0.012μF	51kΩ	510Ω

Note 7: At volume of change = ±12 dB
Q = 1.7
Reference: “AUDIO/RADIO HANDBOOK”, National Semiconductor, 1980, Page 2–61

LM833 MDC MWC
DUAL AUDIO OPERATIONAL AMPLIFIER



Die Layout (A - Step)

Die/Wafer Characteristics

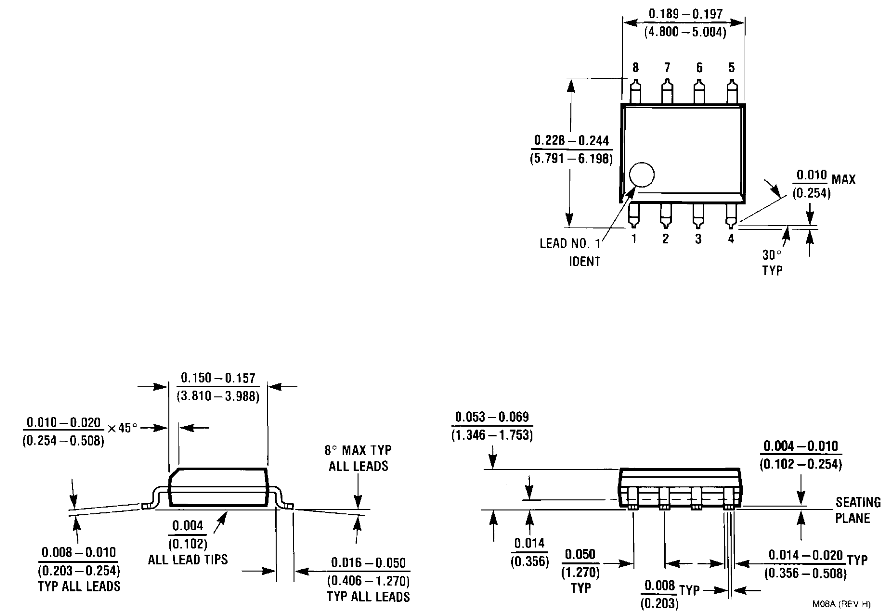
Fabrication Attributes		General Die Information	
Physical Die Identification	LM833A	Bond Pad Opening Size (min)	110µm x 110µm
Die Step	A	Bond Pad Metalization	ALUMINUM
Physical Attributes		Passivation	VOM NITRIDE
Wafer Diameter	150mm	Back Side Metal	BARE BACK
Dise Size (Drawn)	1219µm x 1270µm 48mils x 50mils	Back Side Connection	Floating
Thickness	406µm Nominal		
Min Pitch	288µm Nominal		

Special Assembly Requirements:
Note: Actual die size is rounded to the nearest micron.

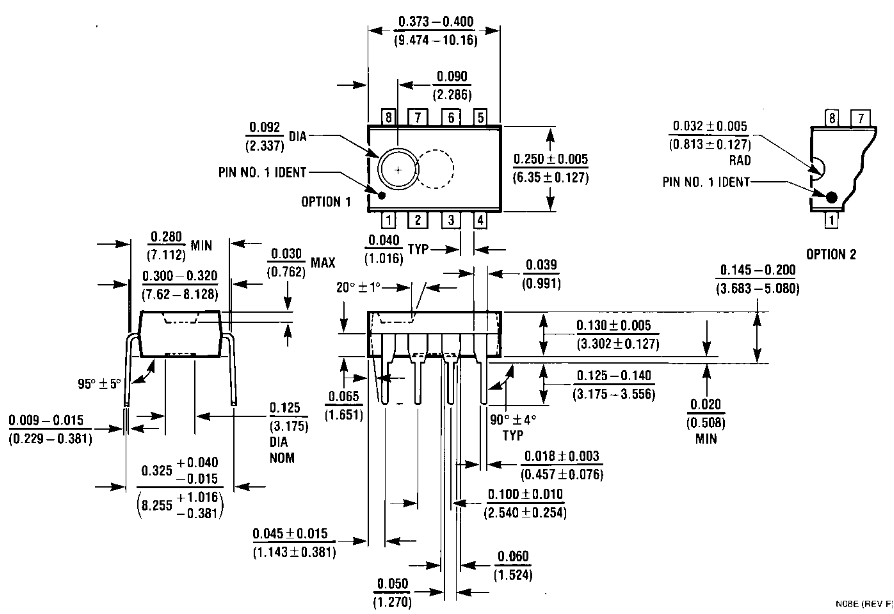
Die Bond Pad Coordinate Locations (A - Step) (Referenced to die center, coordinates in µm) NC = No Connection						
SIGNAL NAME	**PAD NUMBER**	**X/Y COORDINATES**		**PAD SIZE**		
		X	**Y**	**X**		**Y**
OUTPUT A	1	-476	500	110	x	110
INPUT A-	2	-476	-212	110	x	110
INPUT A+	3	-476	-500	110	x	110
VEE-	4	-0	-500	110	x	110
INPUT B+	5	476	-500	110	x	110
INPUT B-	6	476	-212	110	x	110
OUTPUT B	7	476	500	110	x	110
VCC+	8	0	500	110	x	110

IN U.S.A	
Tel #:	1 877 Dial Die 1 877 342 5343
Fax:	1 207 541 6140
IN EUROPE	
Tel:	49 (0) 8141 351492 / 1495
Fax:	49 (0) 8141 351470
IN ASIA PACIFIC	
Tel:	(852) 27371701
IN JAPAN	
Tel:	81 043 299 2308

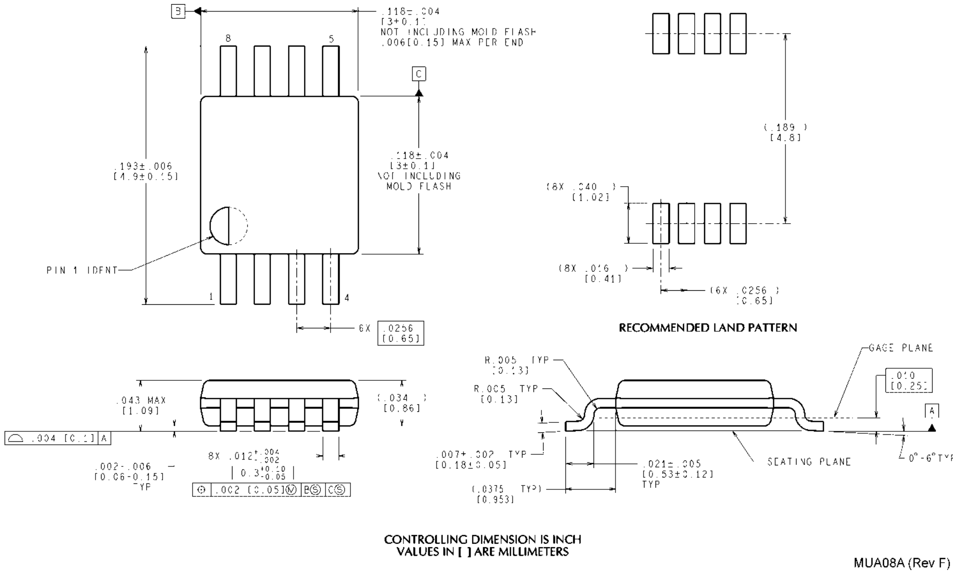
Physical Dimensions inches (millimeters) unless otherwise noted



Molded Small Outline Package (M)
Order Number LM833M or LM833MX
LM833AM, LM833AMX
NS Package Number M08A



Molded Dual-In-Line Package (N)
Order Number LM833N
NS Package Number N08E



8-Lead (0.118” Wide) Molded Mini Small Outline Package
Order Number LM833MM or LM833MMX
NS Package Number MUA08A

Notes

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265