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IN

- No Frequency Compensation Required
- Low Power Consumption
- Short-Circuit Protection
- Offset-Voltage Null Capability
- Wide Common-Mode and Differential Voltage Ranges
- No Latch-Up
- Designed to Be Interchangeable With Fairchild μA747C and μA747M

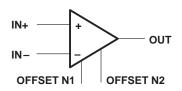
### description

The uA747 is a dual general-purpose operational amplifier featuring offset-voltage null capability. Each half is electrically similar to uA741.

The high common-mode input voltage range and the absence of latch-up make this amplifier ideal for voltage-follower applications. The device is short-circuit protected and the internal frequency compensation ensures stability without external components. A low-value potentiometer may be connected between the offset null inputs to null out the offset voltage as shown in Figure 2.

The uA747C is characterized for operation from  $0^{\circ}$ C to  $70^{\circ}$ C; the uA747M is characterized for operation over the full military temperature range of  $-55^{\circ}$ C to  $125^{\circ}$ C.

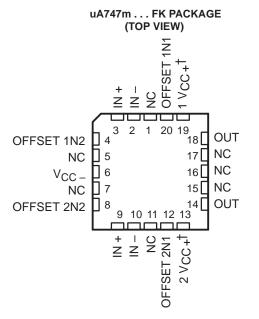
#### symbol (each amplifier)



#### (TOP VIEW) 14 OFFSET 1N1 IN + [ ] 1 V<sub>CC +</sub>† 2 13 OFFSET 1N2 □ Тυο Π 12 J NC Vcc\_[ 11 10 OUT $IN + \Gamma$ 9 2 V<sub>CC+</sub>†

**OFFSET 2N1** 

D, J, N, OR W PACKAGE



NC - No internal connection

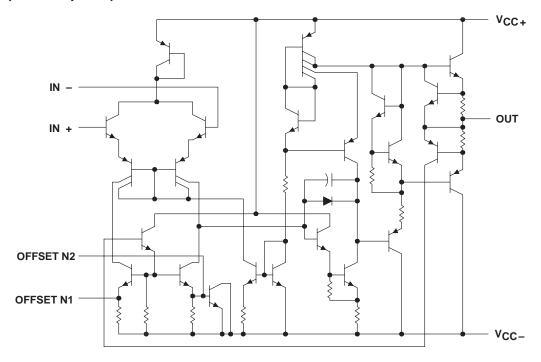
#### **AVAILABLE OPTIONS**

		PACKAGE							
1	V <sub>IO</sub> Max AT 25°C		20-PIN						
TA		SMALL OUTLINE (D)	CERAMIC DIP (J)	PLASTIC DIP (N)	FLAT PACK (W)	CHIP CARRIER (FK)			
0°C									
to 70°C	6 mV	uA747CD	_	uA747CN	_	_			
–55°C									
to 125°C	5 mV	_	uA747MJ	_	uA747MW	uA747MFK			

The D package is available taped and reeled. Add the suffix R to the device type, (i.e., uA747CDR).

<sup>&</sup>lt;sup>†</sup> The two positive supply terminals (1 V<sub>CC +</sub> and 2 V<sub>CC +</sub>) are connected together internally.

## schematic (each amplifier)



### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

		uA747C	uA747M	UNIT
Supply voltage, V <sub>CC+</sub> (see Note 1)		18	22	V
Supply voltage, V <sub>CC</sub> (see Note 1)	oltage, V <sub>CC</sub> _ (see Note 1)			V
Differential input voltage (see Note 2)	Differential input voltage (see Note 2)			V
Input voltage any input (see Notes 1 and 3)		±15	±15	V
Voltage between any offset null terminal (N1/N2) and V <sub>CC</sub> -	±0.5	±0.5	V	
Duration of output short circuit (see Note 4)			unlimited	
Continuous total dissipation	See Dissipation Rating Table			
Operating free-air temperature range			-55 to 125	°C
Storage temperature range			-65 to 150	°C
Case temperature for 60 seconds FK pack			260	°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds	J or W package		300	°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	D or N package	260		°C

- NOTES: 1. All voltage values, unless otherwise noted, are with respect to the midpoint between  $V_{CC}$  + and  $V_{CC}$  -.
  - 2. Differential voltages are at the noninverting input terminal with respect to the inverting input terminal.
  - 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 V, whichever is less.
  - 4. The output may be shorted to ground or either power supply. For the uA747M only, the unlimited duration of the short circuit applies at (or below) 125°C case temperature or 75°C free-air temperature.

#### **DISSIPATION RATING TABLE**

PACKAGE	$T_{\mbox{A}} \leq 25^{\circ}\mbox{C}$ POWER RATING	DERATING FACTOR	DERATE ABOVE T <sub>A</sub>	T <sub>A</sub> = 70°C POWER RATING	T <sub>A</sub> = 125°C POWER RATING
D	800 mW	7.6 mW/°C	45°C	608 mW	_
FK	800 mW	11.0 mW/°C	77°C	800 mW	275 mW
J	800 mW	11.0 mW/°C	77°C	800 mW	275 mW
N	800 mW	9.2 mW/°C	63°C	736 mW	_
W	800 mW	8.0 mW/°C	50°C	640 mW	200 mW



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# electrical characteristics at specified free-air temperature, $V_{\mbox{CC}\pm}$ = $\pm 15~\mbox{V}$

	PARAMETER	TEST CONDITIONS	TA∓	uA747C			uA747M			UNIT	
	PARAWEIER	TEST CONDITIONS†	I AT	MIN	TYP	MAX	MIN	TYP	MAX	UNII	
VIO	Input offset voltage	., .	25°C		1	6		1	5	>/	
	Input offset voltage	VO = 0	Full range			7.5			6	mV	
$\Delta V$ IO(adj)	Offset voltage adjust range		25°C		±15			±15		mV	
L =			25°C		20	200		20	200	nA	
liO	Input offset current		Full range			300			500		
L-	Innut hine aument		25°C		80	500		80	500	A	
IIB	Input bias current		Full range			800			1500	nA	
.,	Common-mode		25°C	±12	±13		±12	±13			
VICR	input voltage range		Full range	±12			±12			V	
	Maximum peak-to-peak	R <sub>L</sub> = 10 kΩ	25°C	24	28		24	28		V	
		R <sub>L</sub> ≥ 10 kΩ	Full range	24			24				
VO(PP)	output voltage swing	R <sub>L</sub> = 2 kΩ	25°C	20	26		20	26			
		$R_L \ge 2 k\Omega$	Full range	20			20				
Δ.	Large-signal differential	$R_L \ge 2 k\Omega$ ,	25°C	25	200		50	200		V/mV	
AVD	voltage amplification	$V_0 = \pm 10 \text{ V}$	Full range	15			25				
rį	Input resistance		25°C	0.3	2		0.3*	2		МΩ	
r <sub>O</sub>	Output resistance	See Note 5	25°C		75			75		Ω	
Ci	Input capacitance		25°C		1.4			1.4		pF	
CMDD	Common-mode rejection ratio	V <sub>IC</sub> = V <sub>ICR</sub>	25°C	70	90		70	90		dB	
CMRR			Full range	70			70				
ksvs	Supply-voltage sensitivity	V <sub>CC</sub> = ± 9 V to ± 15 V	25°C Full range		30	150		30	150	μV/V	
	(ΔV <sub>IO</sub> / ΔV <sub>CC</sub> )					150			150		
los	Short-circuit output current		25°C		±25	±40		±25	±40	mA	
loo	Supply current (each amplifier)	No load	25°C		1.7	2.8		1.7	2.8	mA	
ICC			Full range			3.3			3.3		
D-	Power dissipation	No load, $V_{O} = 0$	25°C		50	85		50	85	mW	
PD	(each amplifier)	No load, $V_0 = 0$	Full range			100			100	11100	
$V_{01}/V_{02}$	Channel separation		25°C		120			120	0	dB	

<sup>†</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified.

# operating characteristics, V<sub>CC</sub> $_{\pm}$ = $\pm$ 15 V, T<sub>A</sub> = 25°C

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
t <sub>r</sub>	Rise time	V. 20 mV D. 2 kO C. 400 nF Con Figure 4		0.3		μs
	Overshoot factor	$V_I = 20 \text{ mV}, R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF, See Figure 1}$		5%		
SR	Slew rate at unity gain	$V_I = 10 \text{ mV}, R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF, See Figure 1}$		0.5		V/μs

 $<sup>\</sup>ddagger$  Full range for uA747C is 0°C to 70°C and for uA747M is  $-55^{\circ}$ C to 125°C.

<sup>\*</sup>On products compliant to MIL-STD-883, Class B, this parameter is not production tested.

NOTE 5: This typical value applies only at frequencies above a few hundred hertz because of the effects of drift and thermal feedback.

#### PARAMETER MEASUREMENT INFORMATION

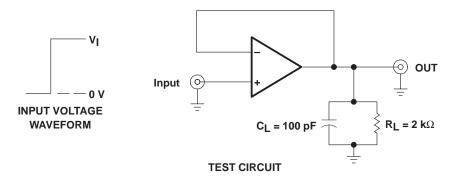


Figure 1. Rise Time, Overshoot, and Slew Rate

### **APPLICATION INFORMATION**

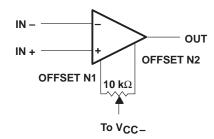


Figure 2. Input Offset Voltage Null Circuit

#### TYPICAL CHARACTERISTICS<sup>†</sup>

# INPUT OFFSET CURRENT vs

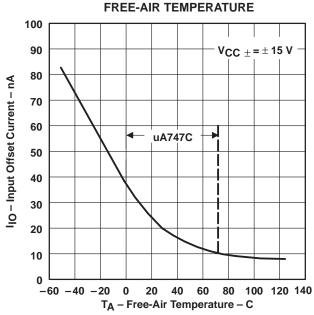


Figure 3

#### **INPUT BIAS CURRENT**

#### vs FREE-AIR TEMPERATURE

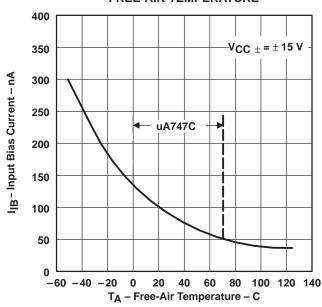


Figure 4

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature range of the particular devices.



#### TYPICAL CHARACTERISTICS

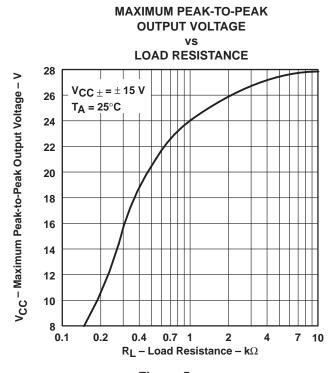


Figure 5

**OPEN-LOOP LARGE-SIGNAL** 

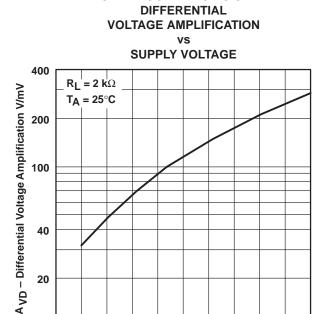


Figure 7

10 12

 $|V_{CC\pm}|$  – Supply Voltage – V

14 16

8

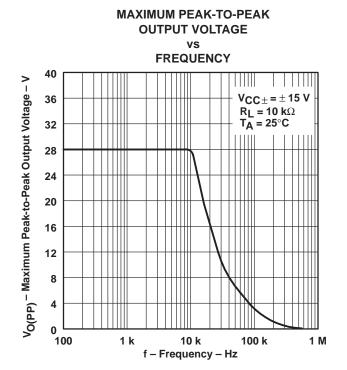


Figure 6

#### **OPEN-LOOP LARGE-SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION**

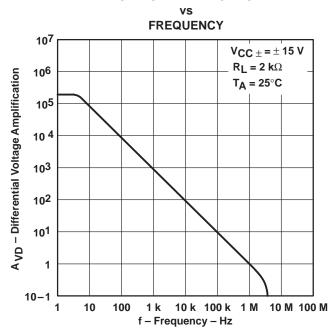


Figure 8



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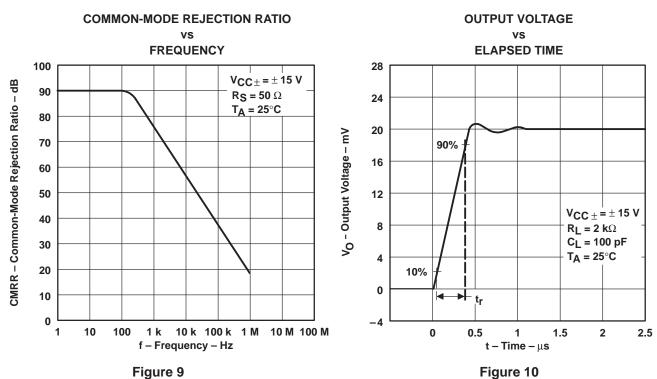
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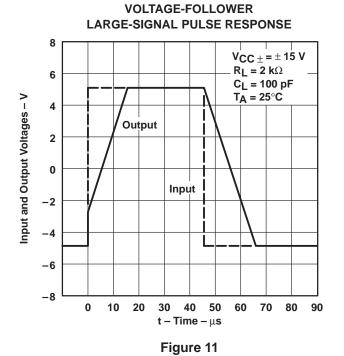
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#### **TYPICAL CHARACTERISTICS**



## rigule 5



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